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APPLICATION NO.	FILING DATE	FIRST NAMED INVENTOR	ATTORNEY DOCKET NO.	CONFIRMATION NO.
10/646,032	08/22/2003	Oliver Dieter Landolt	10011475-1	9255
57299	7590	05/02/2007	EXAMINER	
AVAGO TECHNOLOGIES, LTD.			LEWIS, DAVID LEE	
P.O. BOX 1920				
DENVER, CO 80201-1920			ART UNIT	PAPER NUMBER
			2629	
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			05/02/2007	PAPER

Please find below and/or attached an Office communication concerning this application or proceeding.

The time period for reply, if any, is set in the attached communication.

Office Action Summary	Application No.	Applicant(s)	
	10/646,032	LANDOLT, OLIVER DIETER	
	Examiner	Art Unit	
	David L. Lewis	2629	

-- The MAILING DATE of this communication appears on the cover sheet with the correspondence address --
Period for Reply

A SHORTENED STATUTORY PERIOD FOR REPLY IS SET TO EXPIRE 3 MONTH(S) OR THIRTY (30) DAYS, WHICHEVER IS LONGER, FROM THE MAILING DATE OF THIS COMMUNICATION.

- Extensions of time may be available under the provisions of 37 CFR 1.136(a). In no event, however, may a reply be timely filed after SIX (6) MONTHS from the mailing date of this communication.
- If NO period for reply is specified above, the maximum statutory period will apply and will expire SIX (6) MONTHS from the mailing date of this communication.
- Failure to reply within the set or extended period for reply will, by statute, cause the application to become ABANDONED (35 U.S.C. § 133). Any reply received by the Office later than three months after the mailing date of this communication, even if timely filed, may reduce any earned patent term adjustment. See 37 CFR 1.704(b).

Status

- 1) Responsive to communication(s) filed on 30 January 2007.
- 2a) This action is **FINAL**. 2b) This action is non-final.
- 3) Since this application is in condition for allowance except for formal matters, prosecution as to the merits is closed in accordance with the practice under *Ex parte Quayle*, 1935 C.D. 11, 453 O.G. 213.

Disposition of Claims

- 4) Claim(s) 1-21 is/are pending in the application.
- 4a) Of the above claim(s) _____ is/are withdrawn from consideration.
- 5) Claim(s) 1-15, 19 and 20 is/are allowed.
- 6) Claim(s) 16-18 and 21 is/are rejected.
- 7) Claim(s) _____ is/are objected to.
- 8) Claim(s) _____ are subject to restriction and/or election requirement.

Application Papers

- 9) The specification is objected to by the Examiner.
- 10) The drawing(s) filed on _____ is/are: a) accepted or b) objected to by the Examiner.
 Applicant may not request that any objection to the drawing(s) be held in abeyance. See 37 CFR 1.85(a).
 Replacement drawing sheet(s) including the correction is required if the drawing(s) is objected to. See 37 CFR 1.121(d).
- 11) The oath or declaration is objected to by the Examiner. Note the attached Office Action or form PTO-152.

Priority under 35 U.S.C. § 119

- 12) Acknowledgment is made of a claim for foreign priority under 35 U.S.C. § 119(a)-(d) or (f).
- a) All b) Some * c) None of:
 1. Certified copies of the priority documents have been received.
 2. Certified copies of the priority documents have been received in Application No. _____.
 3. Copies of the certified copies of the priority documents have been received in this National Stage application from the International Bureau (PCT Rule 17.2(a)).

* See the attached detailed Office action for a list of the certified copies not received.

Attachment(s)

- 1) Notice of References Cited (PTO-892)
 2) Notice of Draftsperson's Patent Drawing Review (PTO-948)
 3) Information Disclosure Statement(s) (PTO/SB/08)
 Paper No(s)/Mail Date _____
- 4) Interview Summary (PTO-413)
 Paper No(s)/Mail Date. _____
- 5) Notice of Informal Patent Application
 6) Other: _____

DETAILED ACTION

Claim Rejections - 35 USC § 102

The following is a quotation of the appropriate paragraphs of 35 U.S.C. 102 that form the basis for the rejections under this section made in this Office action:

A person shall be entitled to a patent unless —

(a) the invention was known or used by others in this country, or patented or described in a printed publication in this or a foreign country, before the invention thereof by the applicant for a patent.

1. **Claims 16-18 and 21 are rejected under 35 U.S.C. 102(a) as being anticipated by Nakahira et al. (2003/0146923).**

As in claim 16, Nakahira et al. teaches of a method for driving an electrical device, said method comprising: receiving an input signal, figure 6 item Vin; applying a stored signal to an output transistor in response to said input signal to produce an output signal on an output node connected to said output transistor, figure 6 item 6 and 12; and controlling said output signal on said output node using a difference between a reference current and current coactively fed back from said output node, figure 6 item 211, 221, and Vout.

Wherein Nakahira teaches of in input Vin that causes a controlling means 12 to appropriately apply a signal stored on capacitor 6 to the output transistor node Vout.

As in claim 17, Nakahira et al. teaches of further comprising storing a control signal on said output transistor as said stored signal, figure 6 item 221, wherein a capacitor 221 stores a charge to effect transistor 205.

As in claim 18, Nakahira et al. teaches of wherein said controlling includes generating said reference current using a reference frequency and a reference

voltage, and applying said reference current to a control terminal of said output transistor, figure 6 items 211, 221, 212, and Vout. Wherein the two current sources are applied to the capacitor 221.

As in claim 21, Nakahira et al. teaches of wherein said current source is configured to generate said reference current proportional to a reference voltage and a reference frequency, figure 6 items 211, 221, 212, and Vout. Wherein the two current sources are applied to the capacitor 221, generating said reference current proportional to a reference voltage and a reference frequency.

Allowable Subject Matter

2. Claims 1-15, 19, and 20 are allowed over the prior art of record for the reason cited below.

Response to Arguments

3. Applicant's arguments filed 1/30/2007 are persuasive. Itakura et al, Mortara et al., and Chung all fail to teach or suggest of "a memory operatively connected to said control terminal of said output transistor, said memory being configured to store a signal on said control terminal from a previous cycle in which said output transistor was activated". Therefore claims 1-15 are allowed over the prior art of record. Claims 19 and 20 are also allowed in view of new art Nakahira et al. which however reads on the claim limitations of claims 16-18 and 21. Nakahira et al. also fails to teach of "a memory operatively connected to said control terminal of said output transistor, said memory being configured to store a signal on said control terminal from a previous cycle in which said output transistor was

activated", however claim 16 fails to require said limitation. Claims 16-18 and 21 are therefore rejected in view of Nakahira et al. The 112 second paragraph rejection is withdrawn in view of the Applicants amendments to claims 15 and 16. Applicant's amendment necessitated the new ground(s) of rejection presented in this Office action. Accordingly, this action is made final.

Conclusion

4. Applicant's amendment necessitated the new ground(s) of rejection presented in this Office action. Accordingly, **THIS ACTION IS MADE FINAL**. See MPEP § 706.07(a). Applicant is reminded of the extension of time policy as set forth in 37 CFR 1.136(a). A shortened statutory period for reply to this final action is set to expire THREE MONTHS from the mailing date of this action. In the event a first reply is filed within TWO MONTHS of the mailing date of this final action and the advisory action is not mailed until after the end of the THREE-MONTH shortened statutory period, then the shortened statutory period will expire on the date the advisory action is mailed, and any extension fee pursuant to 37 CFR 1.136(a) will be calculated from the mailing date of the advisory action. In no event, however, will the statutory period for reply expire later than SIX MONTHS from the date of this final action.

5. Any inquiry concerning this communication or earlier communications from the examiner should be directed to **David L. Lewis** whose telephone number is **(571) 272-7673**. The examiner can normally be reached on MT and THF from 8 to 5. If attempts to reach the examiner by telephone are unsuccessful, the examiner's supervisor, Bipin Shalwala, can be reached on **(571) 272-7681**. Any inquiry of a general nature or relating to the status of this application or proceeding should be directed to the Group receptionist whose telephone number is **(571)-273-8300**.

Art Unit: 2629

6. Information regarding the status of an application may be obtained from the Patent Application Information Retrieval (PAIR) system. Status information for published applications may be obtained from either Private PAIR or Public PAIR. Status information for unpublished applications is available through Private PAIR only. For more information about the PAIR system, see <http://pair-direct.uspto.gov>. Should you have questions on access to the Private PAIR system, contact the Electronic Business Center (EBC) at 866-217-9197 (toll-free).

Examiner: David L. Lewis

April 29, 2007

A handwritten signature in black ink, appearing to read "David L. Lewis".